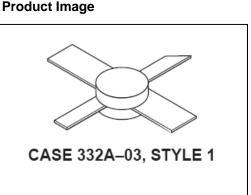
Microwave Pulse Power Silicon NPN Transistor 90W (peak), 960–1215MHz



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Designed for Class B and C common base amplifier applications in short pulse TACAN, IFF, and DME transmitters.

- Guaranteed performance @ 1090 MHz, 50 Vdc Output power = 90 W Peak Minimum gain = 8.4 dB
- 100% tested for load mismatch at all phase angles with 10:1 VSWR
- Industry standard package
- Nitride passivated
- Gold metallized for long life and resistance to metal migration
- Internal input matching for broadband operation



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Base Voltage	V _{CBO}	70	Vdc
Emitter–Base Voltage	V _{EBO}	4.0	Vdc
Collector-Current — Peak (1)	Ι _C	6.0	Adc
Total Device Dissipation @ T _C = 25°C (1) (2) Derate above 25°C	PD	290 1.66	Watts W/∘C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case (3)	R _{eJC}	0.6	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = 25 mAdc, V _{BE} = 0)	V _{(BR)CES}	70	_	_	Vdc
Collector–Base Breakdown Voltage (I _C = 25 mAdc, I _E = 0)	V _{(BR)CBO}	70	-	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 5.0 mAdc, I _C = 0)	V _{(BR)EBO}	4.0	-	_	Vdc
Collector Cutoff Current (V _{CB} = 50 Vdc, I _E = 0)	I _{CBO}	_	_	5.0	mAdc
ON CHARACTERISTICS	ł	1			

ON CHARACTERISTICS

DC Current Gain (4)	h _{FE}	10	30	_	_
(I _C = 2.5 Adc, V _{CE} = 5.0 Vdc)					

NOTES:

1. Pulse Width = 10 µs, Duty Cycle = 1%.

2. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.

3. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.

4. 80 µs Pulse on Tektronix 576 or equivalent.

1

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(continued)

Solutions has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.

Technology Solutions

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ELECTRICAL CHARACTERISTICS - continued (T_C = 25°C unless otherwise noted) Characteristic Symbol Min Unit Тур Max DYNAMIC CHARACTERISTICS Output Capacitance 12 16 рF Cob _ (V_{CB} = 50 Vdc, I_E = 0, f = 1.0 MHz) FUNCTIONAL TESTS (Pulse Width = 10 µs, Duty Cycle = 1.0%) Common-Base Amplifier Power Gain 8.4 10.8 dB Gpr (V_{CC} = 50 Vdc, P_{out} = 90 W pk, f = 1090 MHz) Collector Efficiency 35 40 % η (V_{CC} = 50 Vdc, P_{out} = 90 W pk, f = 1090 MHz) Load Mismatch ψ (V_{CC} = 50 Vdc, P_{out} = 90 W pk, f = 1090 MHz, VSWR = 10:1 All Phase Angles) No Degradation in Power Output Ο + 50 Vdc C2 C3 C4 L1 L2 RF RF DUT INPUT OUTPUT Z8 Z9 Z1 Z3 Z6 Ζ4 Z5 Z2 Ζ7 C1, C2 - 220 pF Chip Capacitor, 100-mil ATC C3 - 0.1 µF C4 - 47 µF/75 V L1, L2 - 3 Turns #18 AWG, 1/8" ID Z1-Z9 — Distributed Microstrip Elements, See Photomaster

Board Material — 0.031" Thick Glass Teflon, $\varepsilon_r = 2.5$

Figure 1. 1090 MHz Test Circuit

2

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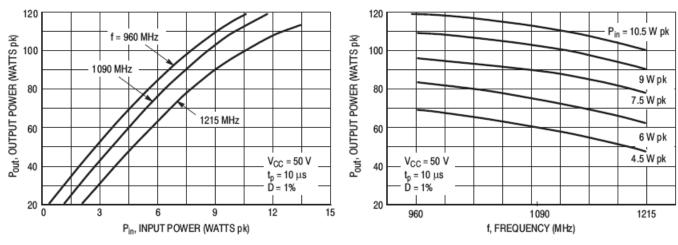


Figure 2. Output Power versus Input Power

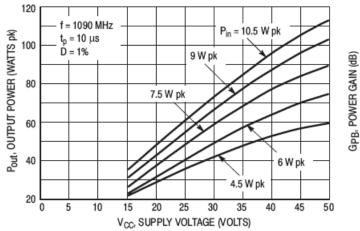


Figure 4. Output Power versus Supply Voltage

Figure 3. Output Power versus Frequency

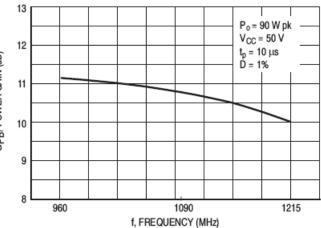


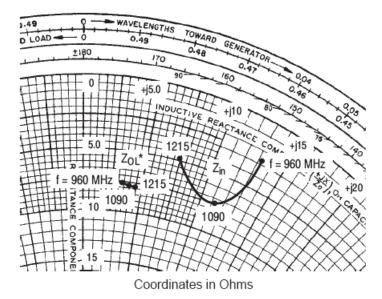
Figure 5. Power Gain versus Frequency

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P _{out} = 90 W pk	V_{CC} = 50 V
t _p = 10 μs	D = 1%

f	Z _{in}	Z _{OL} *
MHz	Ohms	Ohms
960	2.8 + j13.2	7.6 + j3.5
1090	7.4 + j11.4	7.6 + j4.0
1215	4.7 + j7.5	7.7 + j4.5

Z_{OL}* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage, and frequency.



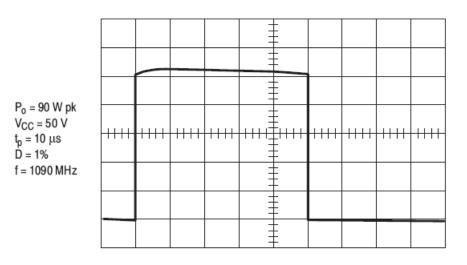


Figure 7. Typical Pulse Performance

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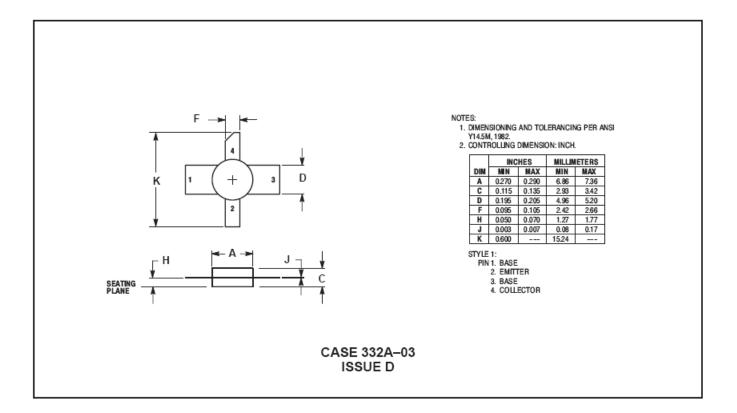


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PACKAGE DIMENSIONS



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